

100

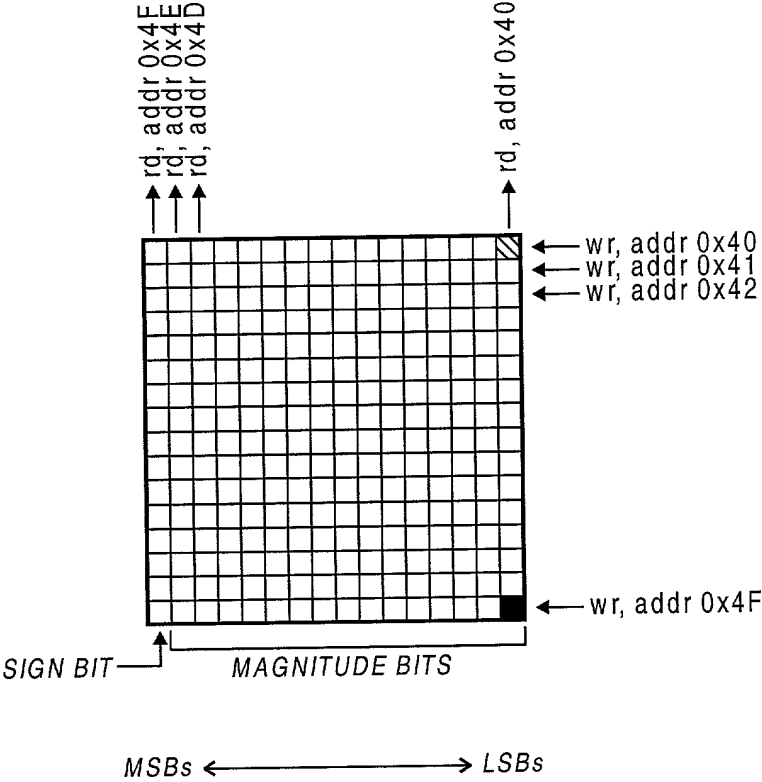
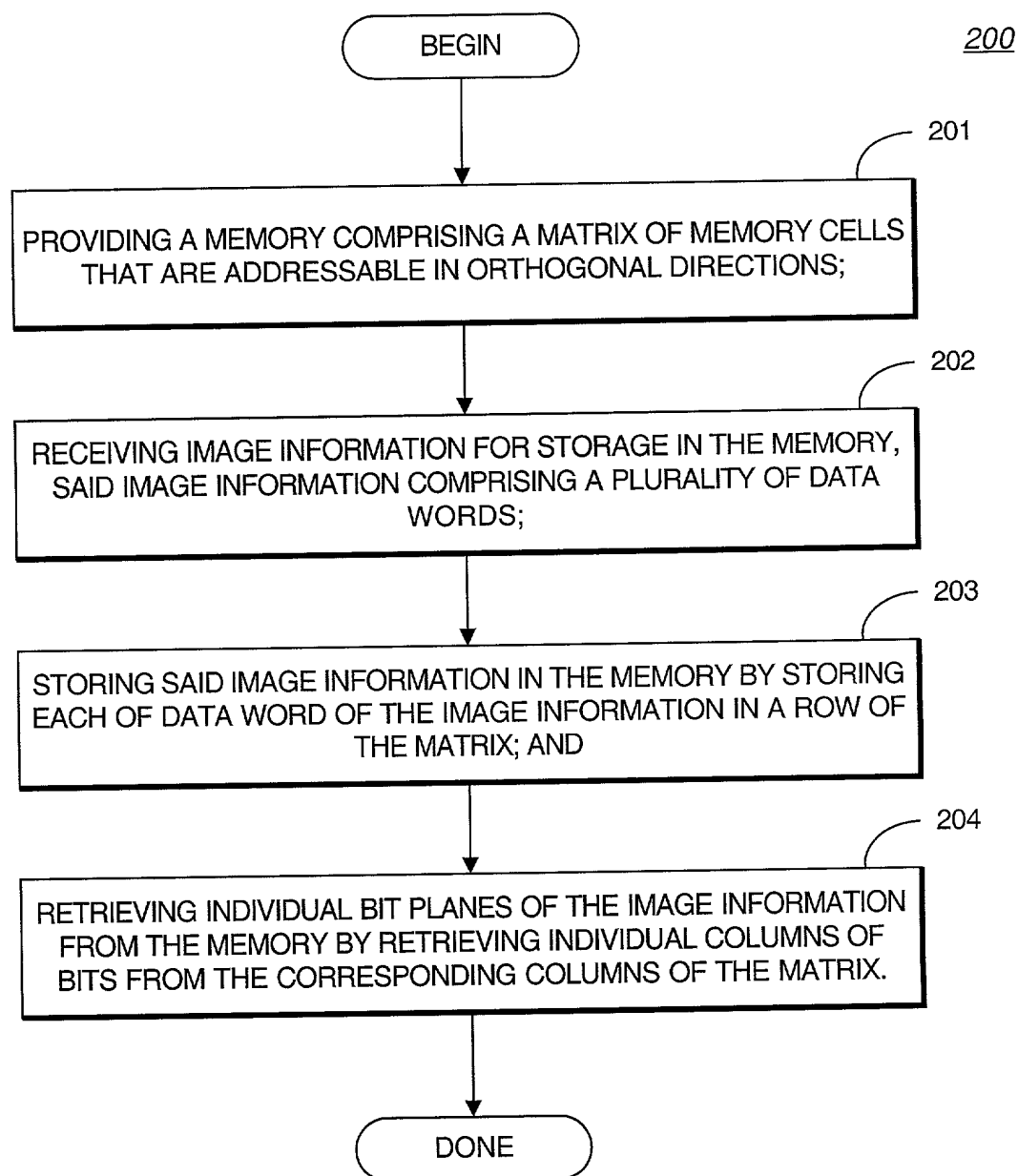
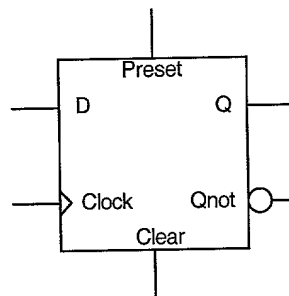


FIG. 1

*FIG. 2*

300

D-type flip-flop

**FIG. 3A**

310

8 bit register

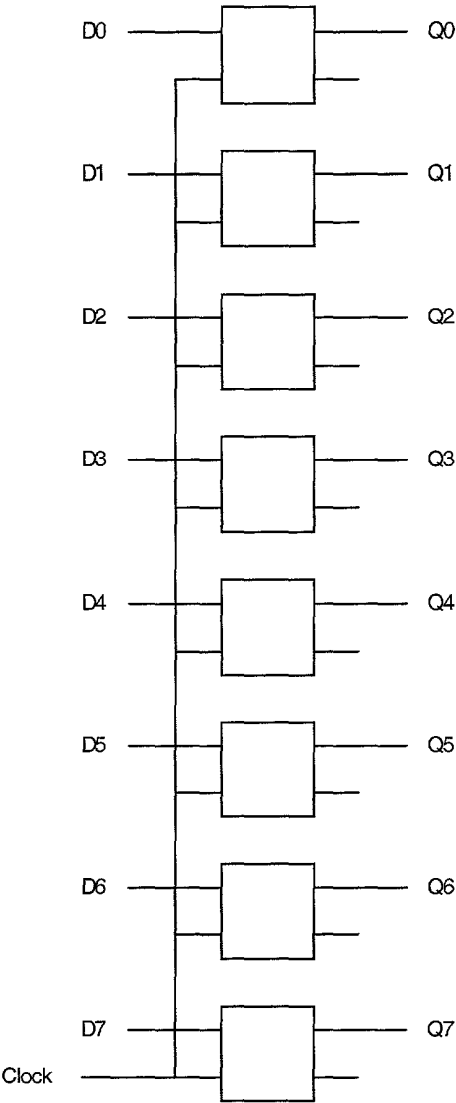


FIG. 3B

320

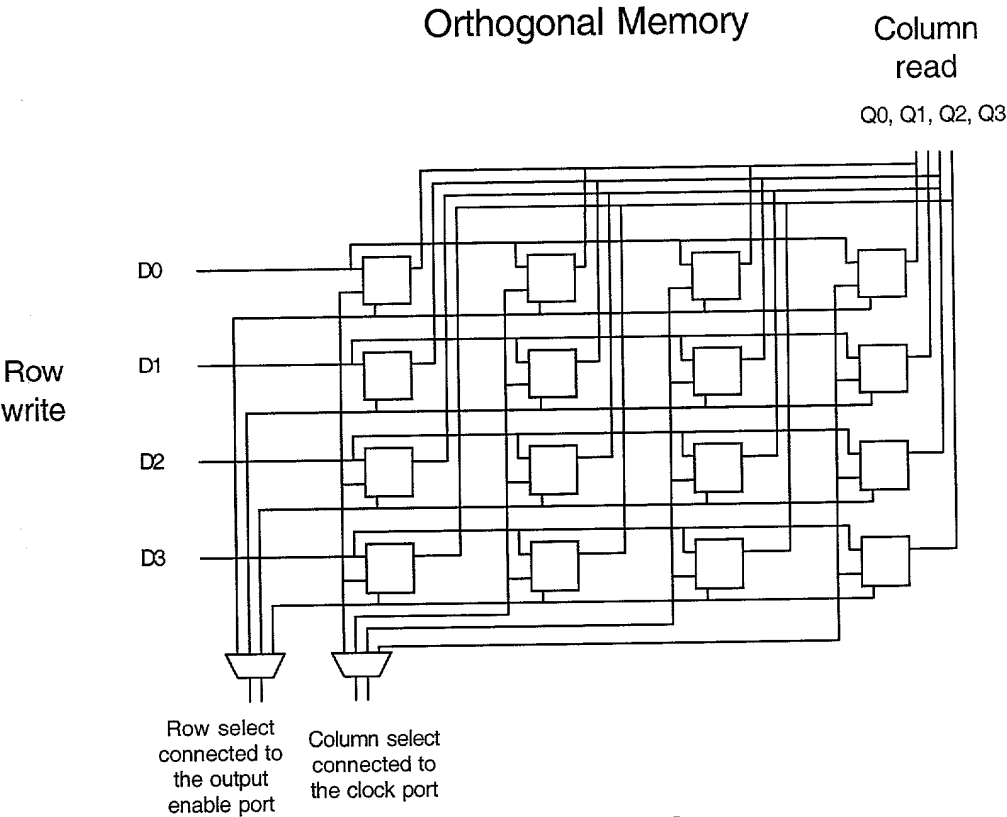


FIG. 3C